General Certificate of Education (A-level) June2013

## Electronics

ELEC2
(Specification 2430)
Unit 2: Further Electronics

## Final

Mark Scheme

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation events which all examiners participate in and is the scheme which was used by them in this examination. The standardisation process ensures that the mark scheme covers the students' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for standardisation each examiner analyses a number of students' scripts: alternative answers not already covered by the mark scheme are discussed and legislated for. If, after the standardisation process, examiners encounter unusual answers which have not been raised they are required to refer these to the Principal Examiner.

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|  | (b) | Formula <br> 120 mV <br> 0.12 V <br> (No unit error if obvious) | $\mathbf{2}$ |  |
| :--- | :--- | :--- | :--- | :--- | :--- |

(a)

| 1 | (d) |  | (No possible ecf from (c)) Calculation $10 \mathrm{kHz}$ | 2 |
| :---: | :---: | :---: | :---: | :---: |
| 2 | (a) | (i) | D to $\overline{\mathbf{Q}}$ <br> Input to CK <br> Outputs Q or $\overline{\mathbf{Q}}$ | 3 |
| 2 | (a) | (ii) | Output half the frequency of the input Output only changes on rising edge of input | 2 |
| 2 | (b) | (i) | Sensible calculation $2.4(\mathrm{kHz})$ | 2 |
| 2 | (b) | (ii) | $\overline{\mathbf{Q}}$ to next clock <br> D to $\overline{\mathbf{Q}}$ <br> AND inputs to 1 and 4 <br> AND output to joined Resets | 4 |
| 3 | (a) |  | Resistor between $+\mathrm{V}_{\mathrm{s}}$ and discharge Resistor between discharge and trigger Trigger and threshold connected C between threshold and OV | 4 |
| 3 | (b) |  | Formula Correct substitution 477.6 Hz (Not 480Hz) | 3 |
| 3 | (c) |  | Formula Substitution $108-109 \mathrm{k} \Omega(108.591 \mathrm{k} \Omega)$ | 3 |



| 5 | (c) |  | 50\% of maximum power output Giving answer of 20 Hz to around 300 kHz (If $70 \% 50 \mathrm{~Hz}-100 \mathrm{kHz}$ then 1 max only) | 2 |
| :---: | :---: | :---: | :---: | :---: |
| 6 | (a) |  | Feedback resistor to inverting input <br> Series inverting input resistor <br> Series non-inverting input resistor <br> Resistor from non- inverting input to 0 V - same value as $\mathrm{R}_{\mathrm{f}}$ <br> All resistors in range $1 \mathrm{k} \Omega$ to $1 \mathrm{M} \Omega$ <br> Must have negative feedback then ratio of gain resistors = 100 | 6 |
| 6 | (b) | (i) | 3 V | 1 |
| 6 | (b) | (ii) | OV <br> No difference <br> The same | 1 |



